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10/511,165	10/14/2004	2816	1250	71971-015

CONFIRMATION NO. 6689

20277
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CORRECTED FILING RECEIPT

Date Mailed: 07/09/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections.

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Power of Attorney: The patent practitioners associated with Customer Number 20277

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/01942 02/19/2004

Foreign Applications

JAPAN 2003-047418 02/25/2003

If Required, Foreign Filing License Granted: 09/22/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/511,165**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit with reduced speed variations

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